

	Subclass	
Class		
ISSUE CLASSIFICATION		

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED	PATENT DATE 03/01/01
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APPLICATION NO. 09/606252	CONT/PRIOR D	CLASS 257 438	SUBCLASS 142	ART UNIT -2811 315	EXAMINER <i>Brook</i>
APPLICANTS Raminda Madurawe David Liu					
TITLE High voltage MOS devices with high gated-diode breakdown voltage					

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					
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ISSUE BATCH NUMBER (Primary Examiner) _____ (Date) (Legal Instruments Examiner) _____ (Date)					
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